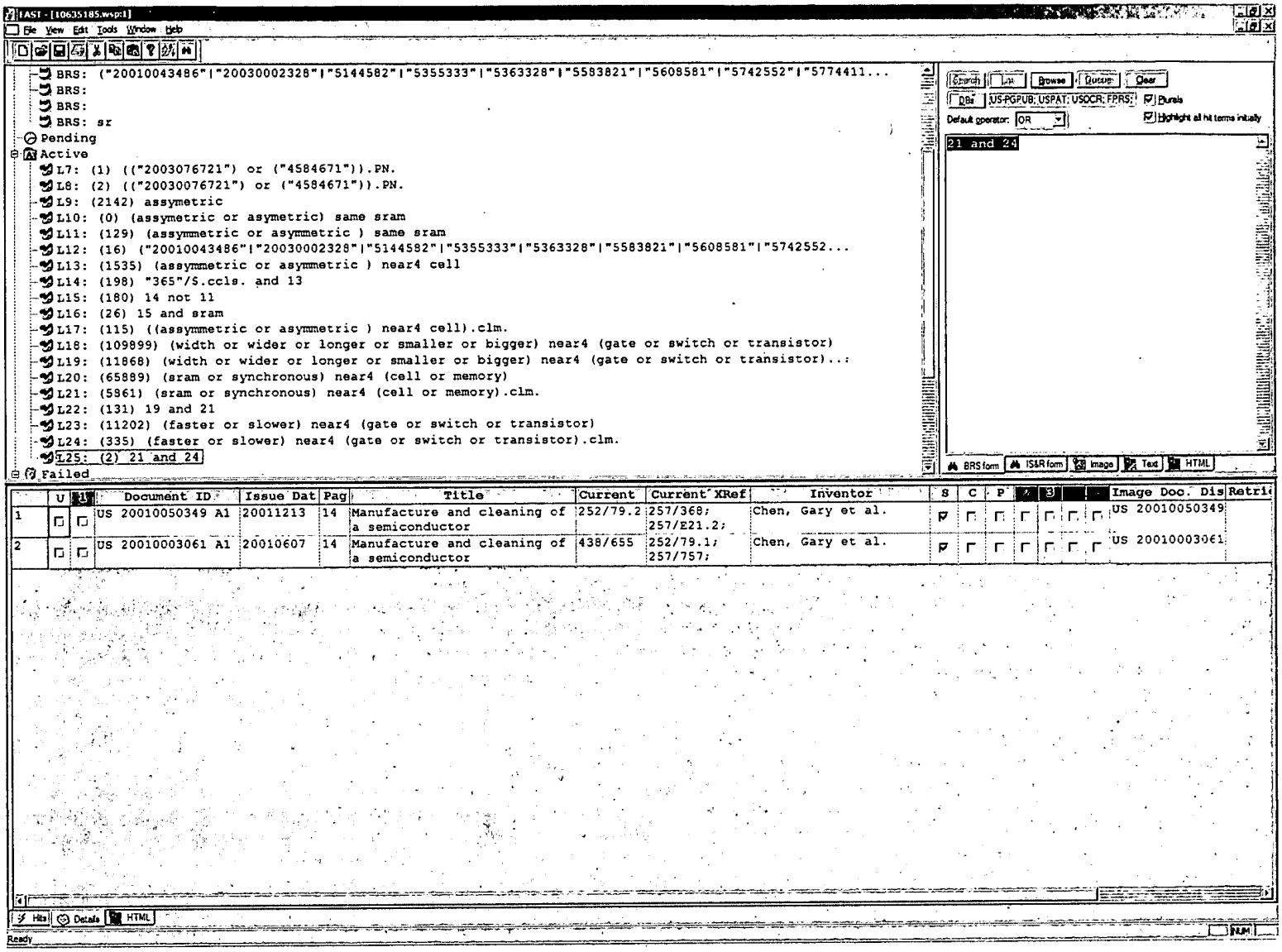
EAST - [10635185.wsp1]
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BRS: ("20010043486"|"20030002328"|"5144582"|"5355333"|"5363328"|"5583821"|"5608581"|"5742552"|"5774411...
BRS:
BRS:
BRS: sr
Pending
Active
L7: (1) (("2003076721") or ("4584671")).PN.
L8: (2) (("20030076721") or ("4584671")).PN.
L9: (2142) assymetric
L10: (0) (assymetric or asymetric) same sram
L11: (129) (assymmetric or asymmetric) same sram
L12: (16) ("20010043486"|"20030002328"|"5144582"|"5355333"|"5363328"|"5583821"|"5608581"|"5742552...
L13: (1535) (assymmetric or asymmetric) near4 cell
L14: (198) "365"/S.ccls. and 13
L15: (180) 14 not 11
L16: (26) 15 and sram
L17: (115) ((assymmetric or asymmetric) near4 cell).clm.
L18: (109899) (width or wider or longer or smaller or bigger) near4 (gate or switch or transistor)
L19: (11868) (width or wider or longer or smaller or bigger) near4 (gate or switch or transistor)...:
L20: (65889) (sram or synchronous) near4 (cell or memory)
L21: (5961) (sram or synchronous) near4 (cell or memory).clm.
L22: (131) 19 and 21
L23: (11202) (faster or slower) near4 (gate or switch or transistor)
L24: (335) (faster or slower) near4 (gate or switch or transistor).clm.
L25: (2) 21 and 24
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Default operator: OR Highlight all hit terms initially
21 and 24
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2 US 20010003061 A1 20010607 14 Manufacture and cleaning of a semiconductor 438/655 252/79.1; 257/757; Chen, Gary et al. US 20010003061
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